



L78LxxAB, L78LxxAC, L78LxxC

Positive voltage regulators

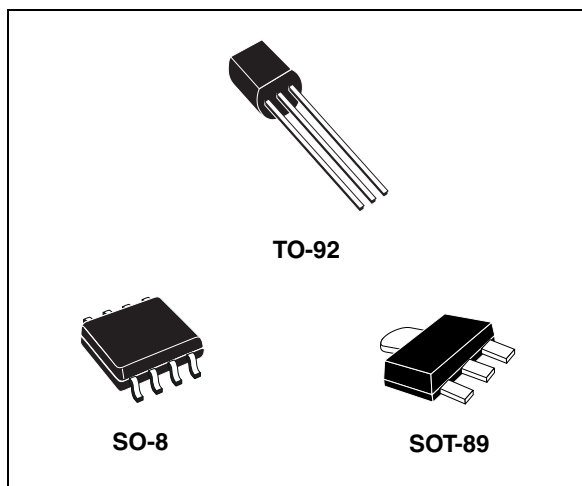
Datasheet — production data

Features

- Output current up to 100 mA
- Output voltages of 3.3; 5; 6; 8; 9; 10; 12; 15; 18; 24 V
- Thermal overload protection
- Short-circuit protection
- No external components are required
- Available in either $\pm 4\%$ (AC) or $\pm 8\%$ (C) selection

Description

The L78Lxx series of three-terminal positive regulators employ internal current limiting and thermal shutdown, making them essentially indestructible. If adequate heat-sink is provided, they can deliver up to 100 mA output current. They are intended as fixed voltage regulators in a wide range of applications including local or on-card regulation for elimination of noise and distribution problems associated with single-point regulation. In addition, they can be used with power pass elements to make high-current voltage regulators. The L78Lxx series used as Zener diode/resistor combination replacement, offers an effective output impedance improvement

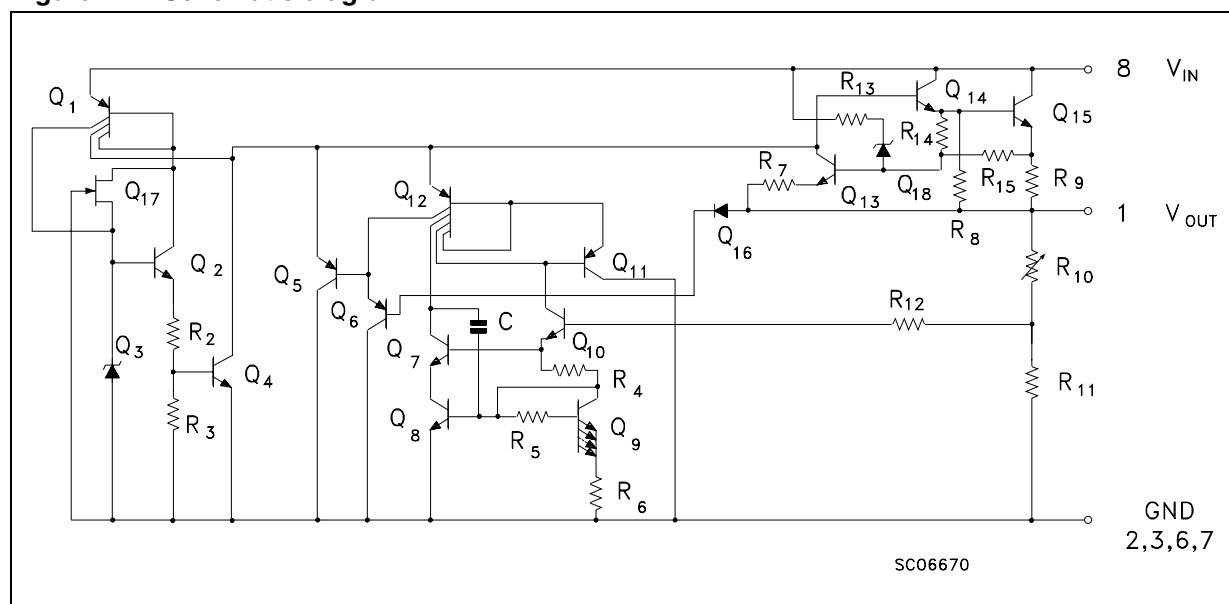


of typically two orders of magnitude, along with lower quiescent current and lower noise.

Table 1. Device summary

Part numbers		
L78L33C	L78L08AC	L78L15C
L78L33AC	L78L08AB	L78L15AC
L78L33AB	L78L09C	L78L15AB
L78L05C	L78L09AC	L78L18C
L78L05AC	L78L09AB	L78L18AC
L78L05AB	L78L10AC	L78L24C
L78L06AC	L78L12C	L78L24AC
L78L06AB	L78L12AC	L78L24AB
L78L08C	L78L12AB	

1 Diagram

Figure 1. Schematic diagram

2 Pin configuration

Figure 2. Pin connection (top view, bottom view for TO-92)

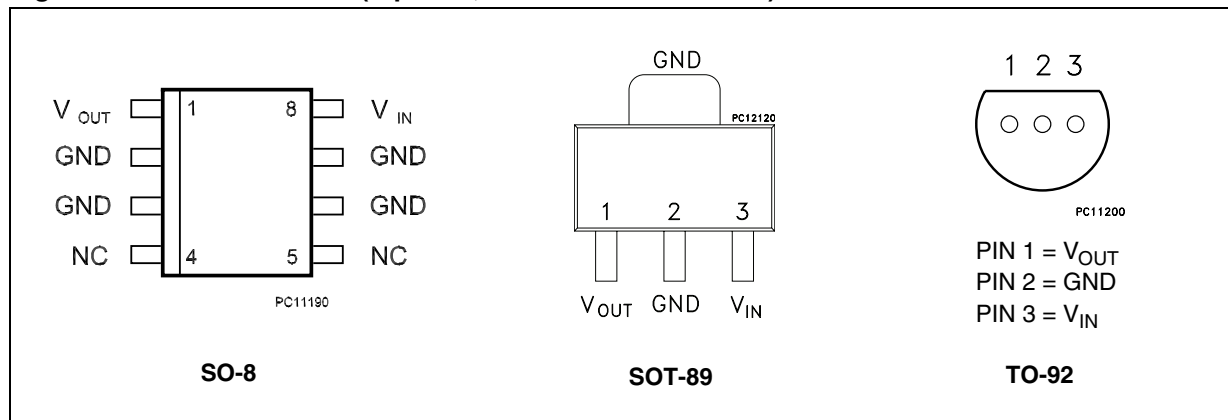
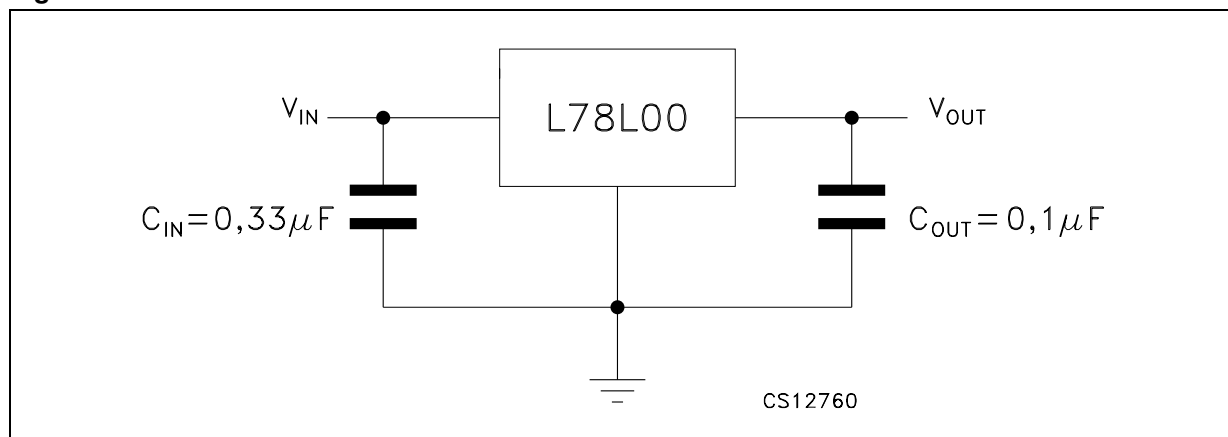


Figure 3. Test circuits



3 Maximum ratings

Table 2. Absolute maximum ratings

Symbol	Parameter		Value	Unit
V_I	DC Input voltage	$V_O = 3.3$ to 9 V	30	V
		$V_O = 12$ to 15 V	35	
		$V_O = 18$ to 24 V	40	
I_O	Output current		100	mA
P_D	Power dissipation		Internally limited ⁽¹⁾	mW
T_{STG}	Storage temperature range		-65 to 150	°C
T_{OP}	Operating junction temperature range	for L78L00AC	0 to 125	°C
		for L78L00AB	-40 to 125	

1. Our SO-8 package used for voltage regulators is modified internally to have pins 2, 3, 6 and 7 electrically communed to the die attach flag. This particular frame decreases the total thermal resistance of the package and increases its ability to dissipate power when an appropriate area of copper on the printed circuit board is available for heat-sinking. The external dimensions are the same as for the standard SO-8.

Table 3. Thermal data

Symbol	Parameter	SO-8	TO-92	SOT-89	Unit
R_{thJC}	Thermal resistance junction-case. (max)	20		15	°C/W
R_{thJA}	Thermal resistance junction-ambient. (max)	55 ⁽¹⁾	200	55 ⁽¹⁾	°C/W

1. Considering 6 cm² of copper Board heat-sink.

4 Electrical characteristics

Refer to the test circuits, $T_J = 0$ to $125\text{ }^{\circ}\text{C}$, $I_O = 40\text{ mA}$, $C_I = 0.33\text{ }\mu\text{F}$, $C_O = 0.1\text{ }\mu\text{F}$ unless otherwise specified.

Table 4. Electrical characteristics of L78L33C ($V_I = 8.3\text{ V}$)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	$T_J = 25\text{ }^{\circ}\text{C}$	3.036	3.3	3.564	V
V_O	Output voltage	$I_O = 1$ to 40 mA , $V_I = 5.3$ to 20 V	2.97		3.63	V
		$I_O = 1$ to 70 mA , $V_I = 8.3\text{ V}$	2.97		3.63	
ΔV_O	Line regulation	$V_I = 5.3$ to 20 V , $T_J = 25\text{ }^{\circ}\text{C}$			150	mV
		$V_I = 6.3$ to 20 V , $T_J = 25\text{ }^{\circ}\text{C}$			100	
ΔV_O	Load regulation	$I_O = 1$ to 100 mA , $T_J = 25\text{ }^{\circ}\text{C}$			60	mV
		$I_O = 1$ to 40 mA , $T_J = 25\text{ }^{\circ}\text{C}$			30	
I_d	Quiescent current	$T_J = 25\text{ }^{\circ}\text{C}$			6	mA
		$T_J = 125\text{ }^{\circ}\text{C}$			5.5	mA
ΔI_d	Quiescent current change	$I_O = 1$ to 40 mA			0.2	mA
		$V_I = 6.3$ to 20 V			1.5	
eN	Output noise voltage	$B = 10\text{ Hz}$ to 100 kHz , $T_J = 25\text{ }^{\circ}\text{C}$		40		μV
SVR	Supply voltage rejection	$V_I = 6.3$ to 16.3 V , $f = 120\text{ Hz}$ $I_O = 40\text{ mA}$, $T_J = 25\text{ }^{\circ}\text{C}$	41	49		dB
V_d	Dropout voltage			1.7		V

Table 5. Electrical characteristics of L78L05C ($V_I = 10\text{ V}$)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	$T_J = 25\text{ }^{\circ}\text{C}$	4.6	5	5.4	V
V_O	Output voltage	$I_O = 1$ to 40 mA , $V_I = 7$ to 20 V	4.5		5.5	V
		$I_O = 1$ to 70 mA , $V_I = 10\text{ V}$	4.5		5.5	
ΔV_O	Line regulation	$V_I = 8.5$ to 20 V , $T_J = 25\text{ }^{\circ}\text{C}$			200	mV
		$V_I = 9$ to 20 V , $T_J = 25\text{ }^{\circ}\text{C}$			150	
ΔV_O	Load regulation	$I_O = 1$ to 100 mA , $T_J = 25\text{ }^{\circ}\text{C}$			60	mV
		$I_O = 1$ to 40 mA , $T_J = 25\text{ }^{\circ}\text{C}$			30	
I_d	Quiescent current	$T_J = 25\text{ }^{\circ}\text{C}$			6	mA
		$T_J = 125\text{ }^{\circ}\text{C}$			5.5	mA
ΔI_d	Quiescent current change	$I_O = 1$ to 40 mA			0.2	mA
		$V_I = 8$ to 20 V			1.5	
eN	Output noise voltage	$B = 10\text{ Hz}$ to 100 kHz , $T_J = 25\text{ }^{\circ}\text{C}$		40		μV

Table 13. Electrical characteristics of L78L33AB and L78L33AC ($V_I = 8.3$ V) (continued)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
ΔV_O	Load regulation	$I_O = 1$ to 100 mA, $T_J = 25$ °C			60	mV
		$I_O = 1$ to 40 mA, $T_J = 25$ °C			30	
I_d	Quiescent current	$T_J = 25$ °C			6	mA
		$T_J = 125$ °C			5.5	mA
ΔI_d	Quiescent current change	$I_O = 1$ to 40 mA			0.1	mA
		$V_I = 6.3$ to 20 V			1.5	
eN	Output noise voltage	B = 10 Hz to 100 kHz, $T_J = 25$ °C		40		μ V
SVR	Supply voltage rejection	$V_I = 6.3$ to 16.3 V, $f = 120$ Hz $I_O = 40$ mA, $T_J = 25$ °C	41	49		dB
V_d	Dropout voltage			1.7		V

$T_J = 0$ to 125 °C for L78L05AC, $T_J = -40$ to 125 °C for L78L05AB.

Table 14. Electrical characteristics of L78L05AB and L78L05AC ($V_I = 10$ V)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_O	Output voltage	$T_J = 25$ °C	4.8	5	5.2	V
V_O	Output voltage	$I_O = 1$ to 40 mA, $V_I = 7$ to 20 V	4.75		5.25	V
		$I_O = 1$ to 70 mA, $V_I = 10$ V	4.75		5.25	
ΔV_O	Line regulation	$V_I = 7$ to 20 V, $T_J = 25$ °C			150	mV
		$V_I = 8$ to 20 V, $T_J = 25$ °C			100	
ΔV_O	Load regulation	$I_O = 1$ to 100 mA, $T_J = 25$ °C			60	mV
		$I_O = 1$ to 40 mA, $T_J = 25$ °C			30	
I_d	Quiescent current	$T_J = 25$ °C			6	mA
		$T_J = 125$ °C			5.5	mA
ΔI_d	Quiescent current change	$I_O = 1$ to 40 mA			0.1	mA
		$V_I = 8$ to 20 V			1.5	
eN	Output noise voltage	B = 10 Hz to 100 kHz, $T_J = 25$ °C		40		μ V
SVR	Supply voltage rejection	$V_I = 8$ to 18 V, $f = 120$ Hz $I_O = 40$ mA, $T_J = 25$ °C	41	49		dB
V_d	Dropout voltage			1.7		V

$T_J = 0$ to 125 °C for L78L06AC, $T_J = -40$ to 125 °C for L78L06AB.

Table 25. Marking information (continued)

Part numbers	Marking	Packages	Output voltages
L78L24CD	78L24	SO8	24 V
L78L33ABD	78L33B	SO8	3.3 V
L78L33ABUTR	8A	SOT 89	3.3 V
L78L33ABZ-AP	L78L33	TO 92	3.3 V
L78L33ACD	78L33A	SO8	3.3 V
L78L33ACD13TR	78L33A	SO8	3.3 V
L78L33ACUTR	8A	SOT 89	3.3 V
L78L33ACZ	L78L33	TO 92	3.3 V
L78L33ACZ-AP	L78L33	TO 92	3.3 V
L78L33ACZTR	L78L33	TO 92	3.3 V
L78L33CD	78L33	SO8	3.3 V
L78L33CD-TR	78L33	SO8	3.3 V